BST4628

1. Product Overview

The BST4628 is a complete dual-channel 8A output switch mode DC/DC power supply. The switch controller, power FETs, inductor and all supporting components are built into the package. The BST4628 operates over an input voltage range of 4.5V to 26.5V and supports two output voltages ranging from 0.6V to 5.5V (set by a single external resistor). The device's high efficiency design is capable of providing 8A of continuous current to each output. Only a small amount of input and output capacitors are required.

The device supports frequency synchronization, multi-phase operation, burst mode operation, and output voltage tracking for power rail sequencing, and has a built-in temperature diode to monitor device temperature. The use of high switching frequency and a current architecture enables fast transient response to line and load changes without sacrificing stability.

Fault protection features include overvoltage and overcurrent protection. The power module is available in a proprietary space-saving and thermally enhanced 15mm×15mm×4.32mm LGA package and 15mm×15mm×4.92mm BGA package with an integrated top heat sink. The BST4628 has a SnPb (BGA) or RoHS compliant pin finish.

2. Product Features

- Complete Standalone Dual-Output Power Supply.
- Dual channel 8A or single channel 16A output.
- Wide input voltage range: 4.5V to 26.5V.
- Output voltage range: 0.6V to 5.5V.
- ±1.5% maximum total DC output error.
- Differential Remote Sense Amplifier.
- Current Mode Control/Fast Transient Response.
- Adjustable switching frequency.
- Overcurrent Foldback Protection.
- Frequency synchronization.
- Internal temperature sensing diode output.
- Output overvoltage protection.
- Heat-resistant enhanced (15mm×15mm×4.32mm) LGA package (15mm×15mm×4.92mm) BGA package.

3. Functional Block Diagram

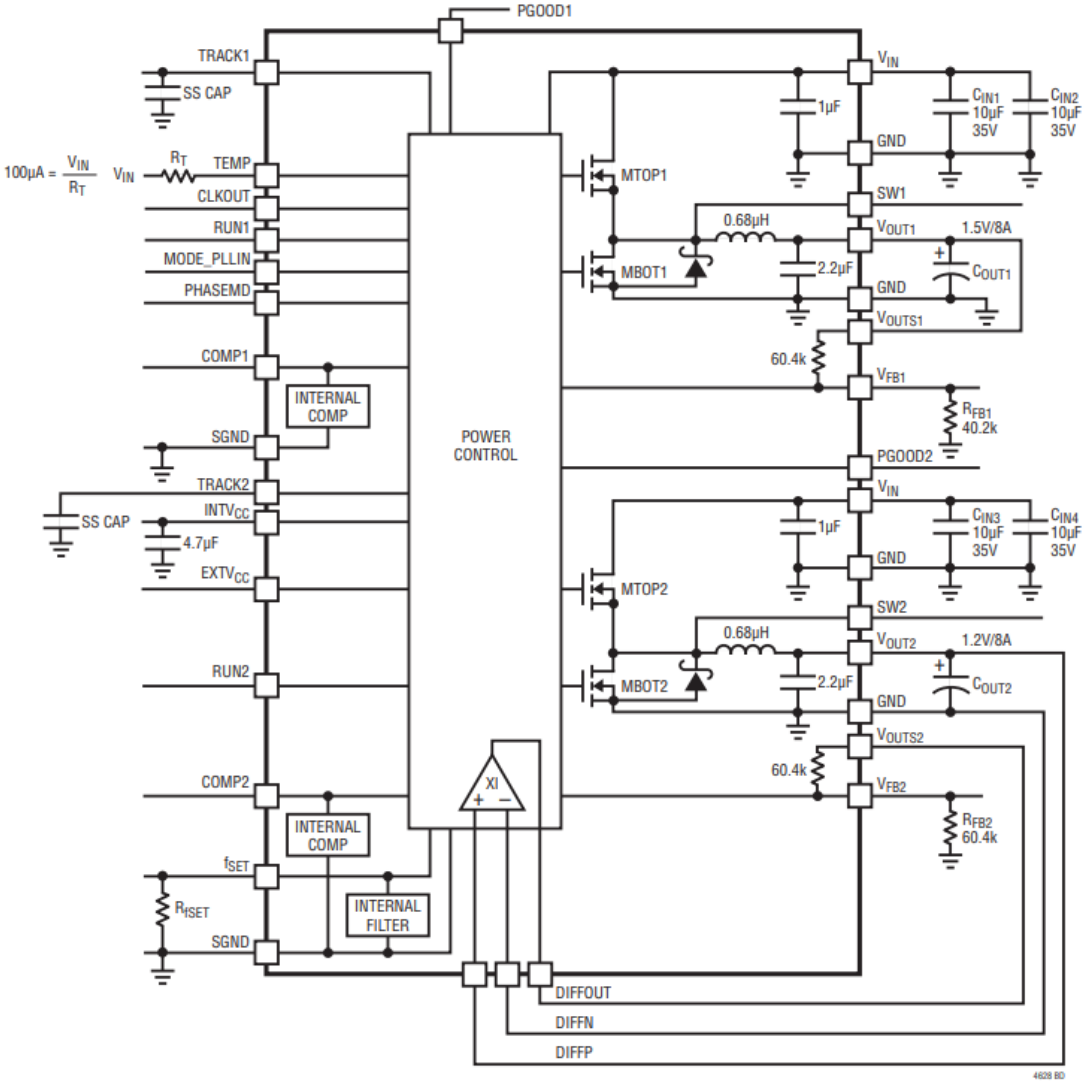

Figure 1 Functional block diagram

4. Pin Information

Pin Assignment

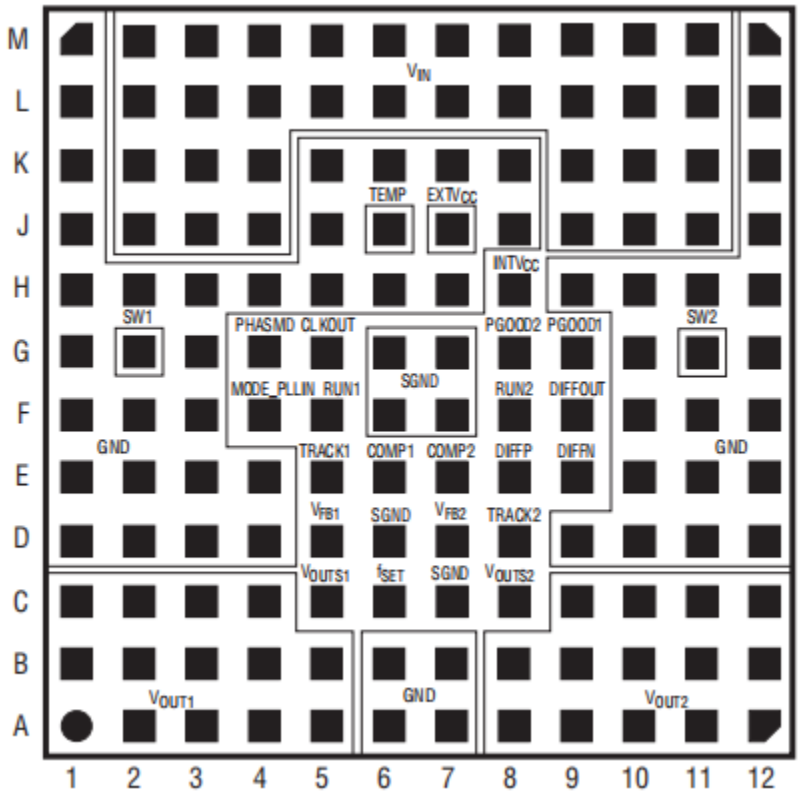

Figure 2BST4628 pinout

Pin Description

Table 1Pin Description

Pin number	Pin Symbols	Functional Description
A1 A2 A3 A4 A5 B1 B2 B3 B4 B5 C1 C2 C3 C4	VOUT1	1 channel output
A6 A7 B6 B7 D1 D2 D3 D4 D9 D10 D11 D12 E1 E2 E3 E4 E10 E11 E12 F1 F2 F3 F10 F11 F12 G1 G3 G10 G12 H1 H2 H3 H4 H5 H6 H7 H9 H10 H11 H12 J1 J5 J8 J12 K1 K5 K6 K7 K8 K12 L1 L12 M1 M12	GND	Power ground pin
A8 A9 A10 A11 A12 B8 B9 B10 B11 B12 C9 C10 C11 C12	VOUT2	2 channel output
E9	DIFFN	The negative input of the differential amplifier
E8	DIFFP	The positive input of the differential amplifier
F8	DIFFOUT	Differential amplifier output
F9	RUN2	2-channel enable control
G8	PGOOD2	2-channel power good indicator
G9	PGOOD1	1-channel power good indicator
G11	SW2	2-channel SW terminal
H8	INTVCC	Internal regulator output
J2 J3 J4 J9 J10 J11 K2 K3 K4 K9 K10 K11 L2 L3 L4 L5 L6 L7 L8 L9 L10 L11 M2 M3 M4 M5 M6 M7 M8 M9 M10 M11	VIN	Input Power
J7	EXTVCC	External power input
J6	TEMP	Temperature sensor comparator input
G5	CLKOUT	Clock output
G2	SW1	1 channel SW terminal
G4	PHASMD	Clock phase difference control terminal
F4	MODE_PLLIN	Operating mode selection and external clock
E5	TRACK1	Regulator 1 soft-start input and output voltage tracking
D5	VFB1	1 channel feedback voltage
D7	VFB2	2-channel feedback voltage
C5	VOUTS1	1 channel output
C6	fSET	Frequency setting port
C7 D6 F6 F7 G6 G7	SGND	Signal Ground
C8	VOUTS2	2 channel output
D8	TRACK2	Regulator 2 soft-start input and output voltage tracking
E6	COMP1	1-channel error amplifier output
E7	COMP2	2-channel error amplifier output
F5	RUN1	1 channel enable control terminal
F9	RUN2	2 channel enable control terminal

5. Absolute Maximum Ratings and Recommended Operating Ranges

Absolute Maximum Ratings

Power supply voltage (V_{IN}) .....-0.3 V～28V
VSW1, VSW2 voltage to ground (V_{SW}) ..... -1 V～28 V
PGOOD1, PGOOD1, RUN1, RUN2, INTV CC , EXTV CC voltage to ground ..... -0.3V～6V
MODE_PLLIN, f SET , TRACK1, TRACK2, DIFFOUT, PHASMD ..... -0.3V～+INTV CC
V OUT1 , V OUT2 , V OUTS1 , V OUTS2 ..... -0.3V～6V
DIFFP, DIFFN voltage to ground .....- 0.3V～+INTV CC
COMP1, COMP2, V FB1 , V FB2 voltage to ground ..... -0.3V ～2.7V
INTV CC peak output current 100mA
Internal operating temperature range..... -40°C～+125°
C Storage temperature range .....-55°C ～125°C
Maximum package temperature ..... 245°C

Recommended working range

Power supply voltage (V_{IN}) .....4.5 V～16V
Output voltage .....0.6V~5.5V
Working environment temperature (*TA*) .....`-45℃～125℃

6. Electrical characteristics parameters

Table 2Electrical characteristics
(V_{IN} = 12V ,V RUN1 , V RUN2 = 5V, T_A = +25°C, unless otherwise specified)

parameter	symbol	strip Item	Standard requirements			unit
			Minimum	typical	maximum	
Input voltage	V_{IN}		4.5		26.5	V
Output voltage	*VOUT*		0.6		5.5	V
Under load, the output voltage changes linearly	$V_{OUT1(DC)}$, $V_{OUT2(DC)}$	C_{IN}=22μF×3, C_{OUT}=100μF×1 Ceramic, 470μF POSCAP, MODE_PLL$_{IN}$ =GND, RFB1, RFB2 = 40.2k, V_{IN} = 4.5V to 26.5V, I_{OUT} = 0A to 8A	1.477	1.5	1.523	V
Input Specifications						
RUN pin switching threshold	V_{RUN1} , V_{RUN2}	RUN Ris$_{IN}$g	1.1	1.25	1.40	V
RUN pin hysteresis voltage	$V_{RUN1HYS}$, $V_{RUN2HYS}$			150		mV
Starting instantaneous current	$I_{INRUSH(VIN)}$	I_{OUT} = 0A, C_{IN} = 22μF × 3, C_{OUT}= 100μF, 470μF POSCAP V_{OUT}1 = 1.5V, V_{OUT}2 = 1.5V, V_{IN} = 12V, TRACK = 0.01μF		1		A
Input bias current	$I_{Q(VIN)}$	V_{IN}=12V,V_{OUT}=1.5V, Burst Mode Operation		5		mA
		V_{IN}=12V,V_{OUT}=1.5V, Pulse-Skipp$_{IN}$g Mode		1 5		mA
		V_{IN}=12V,V_{OUT}=1.5V,Switch$_{IN}$g Cont$_{IN}$uous		6 5		mA
		Shutdown, RUN = 0, V_{IN} = 12V		6 0		1
Input Current	$I_{S(VIN)}$	V_{IN}=4.75V,V_{OUT}=1.5V,I_{OUT} = 8A		2.9		A
		V_{IN}=12V, V_{OUT} = 1.5V, I_{OUT} = 8A		1 .18		A
		V_{IN}= 26.5V,V_{OUT}=1.5V,I_{OUT}= 8A		0.575		A

parameter	symbol	strip Item	Standard requirements			unit
			Minimum	typical	maximum	
Output specifications						
Output continuous current range	$I_{OUT1(DC)}$ $I_{OUT2(DC)}$	V_{IN}=12V,V_{OUT}=1.5V	0		8	A
Linear regulation accuracy	$\Delta V_{OUT1(LINE)}$ / V_{OUT1} $\Delta V_{OUT2(LINE)}$ / V_{OUT2}	V_{OUT}= 1.5V, V_{IN} from 4.5V to 26.5V I_{OUT} = 0A for Each output		0.01	0.04	% V
Load regulation accuracy	ΔV_{OUT1} / V_{OUT1} ΔV_{OUT2} / V_{OUT2}	For Each output, V_{OUT} = 1.5V, 0A to 8A, V_{IN} = 12V		0.15	0.3	%
Output ripple voltage	$V_{OUT1(AC)}$ $V_{OUT2(AC)}$	For Each output, I_{OUT} = 0A, C_{OUT} = 100μF×3/X7R/Ceramic, 470μF POSCAP, V_{IN} = 12V, V_{OUT} = 1.5V, Frequency = 400kHz		1 5		mV P-P
Output ripple voltage frequency	f_S (single channel)	V_{IN}=12V,V_{OUT}=1.5V,fSET=2.5V		780		kHz
SYNC capture frequency range	f_{SYNC} (single channel)		400		780	kHz
Turn on overshoot voltage	$\Delta V_{OUTSTART}$ (Single channel)	C_{OUT} = 100μF X5R Ceramic, 470μF POSCAP, V_{OUT} = 1.5V, I_{OUT} = 0A V_{IN} = 12V		10		m V
Opening time	t_{START} (single channel)	C_{OUT} = 100μF X5R Ceramic, 470μF POSCAP,No Load, TRACK/SS with 0.01μF to GND,V_{IN} = 12V		5		m s
Peak deviation under dynamic load	$\Delta V_{OUT(LS)}$ (Single channel)	Load: 0% to 50% to 0% of Full Load C_{OUT}=22μF×3/X5R/Ceramic, 470μF POSCAP, V_{IN} = 12V, V_{OUT} = 1.5V		30		m V
Step settling time under dynamic load	t_{SETTLE} (Single channel)	Load: 0% to 50% to 0% of Full Load, V_{IN}=12V, C_{OUT}=100μF, 470μF POSCAP		20		us
Output current limit	$\Delta V_{OUT(LS)}$ (Single channel)	V_{IN} = 12V, V_{OUT} = 1.5V		15		A
Control section						
Regulated feedback voltage	*VFB1* , *VFB2*	I_{OUT}=0A,V_{OUT}=1.5V	0.592	0.600	0.606	V
	I_{FB}			-5	-20	nA
Feedback overvoltage lockout	V_V		0.64	0.66	0.68	V
Track pin soft-start pull-up current	*TRACK1(I)* *TRACK2(I)*	TRACK1 (I),TRACK2 (I) Start at 0V	1	1.25	1.5	1
Undervoltage Lockout	UVLO	V_{IN} Falling		3.3		V
		V_{IN} Rising		3.9		V
Undervoltage Lockout Hysteresis	UVLOHYS			0.6		V
Minimum opening time	$t_{ON(MIM)}$			90		n s

parameter	symbol	strip Item	Standard requirements			unit
			Minimum	typical	maximum	
The resistor values between VOUTS1, VOUTS2 , and VFB1, VFB2 outputs	*RFBHI1* , *RFBHI2*		60.05	60.4	60.75	kΩ
PGOOD pin low level	V PGOOD1 , V PGOOD2 Low	IPGOOD = 2mA		0.1	0.3	V
PGOOD Pin Leakage Current	I PGOOD	VPGOOD = 5V			± 5	1
PGOOD Trip Level	V PGOOD	VFB with Respect to Set OUTput Voltage				
		VFB RampING Negative		- 10		%
		VFB RampING Positive		10		%
INTVCC Linear Regulator						
Internal V CC voltage	V INTVCC	6V < V_{IN} < 26.5V	4.8	5	5.2	V
INTVCC load regulation	V INTVCC Load Regulation	ICC = 0mA to 50mA		0.5	2	%
EXTVCC switching voltage	V EXTVCC	EXTVCC RampING Positive	4.5	4.7		V
EXTVCC Pull-Down	V EXTVCC(DROP)	ICC = 20mA, VEXTVCC = 5V		50	100	m V
EXTVCC Hysteresis	V EXTVCC(HYST)			200		m V
Oscillators and Phase-Locked Loops						
Normal frequency	Frequency Nominal	fSET = 1.2V	450	500	550	kHz
Minimum frequency	Frequency Low	fSET = 0V	210	250	290	kHz
Maximum frequency	Frequency High	fSET > 2.4V, Up to INTVCC	700	780	860	kHz
Frequency setting current	fSET		9	10	11	1
MODE_PLLIN input resistance	R MODE_PLLIN			250		kΩ
VOUT1 Phase	CLKOUT	PHASMD = GND		60		Deg
		PHASMD = Float		90		Deg
		PHASMD = INTVCC		120		Deg
Clock high level	CLK High		2			V
Clock low level	CLK Low				0.2	V
Error Amplifier						
Gain	A V Differential Amplifier			1		V
Input resistance	R IN	Measured at DIFFP INput		80		kΩ
Offset voltage	V OS	VDIFFP=$VDIFF_{OUT}$=1.5V, $IDIFF_{OUT}$ = 100μA			3	m V
Power Supply Rejection Ratio	PSRR Differential Amplifier	5V < V_{IN} < 20V		90		dB
Maximum output current	I CL			3		mA
Maximum output voltage	V OUT(MAX)	$IDIFF_{OUT}$ = 300μA	INTVCC – 1.4			V
Gain Bandwidth	GBW			3		MHz

7. Dimensions

LGA 144-LEAD

This chip adopts LGA 144-LEAD package, with the size of 15mm×15mm×4.32mm. The product dimensions are as follows:

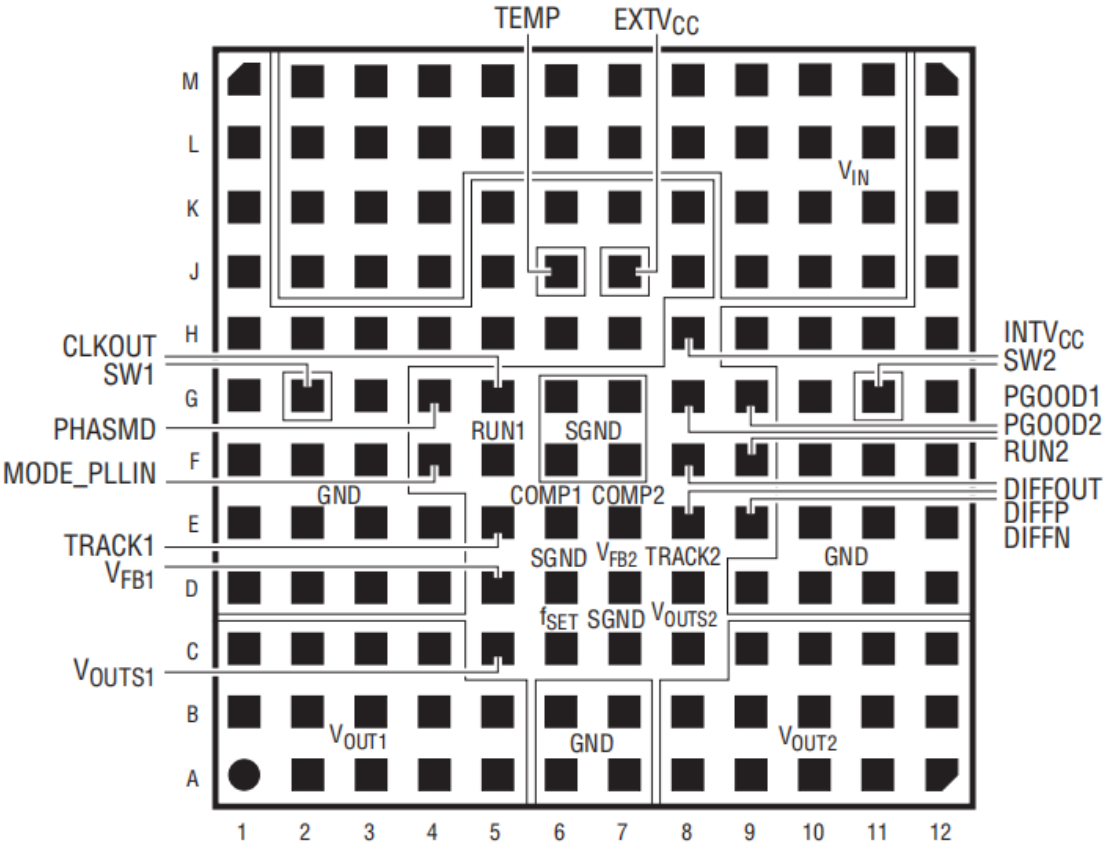

Figure 3Schematic diagram of the bottom surface of the LGA package

8. Ordering Information

Table 3Product ordering information

model	Package	Packaging materials	Quality Grade	Detailed specifications	Product Status
BST4628	LGA 144-LEAD	Plastic packaging	Military Temperature	N / A	In research