

BST14006

1. Overview

The BST14006 is a PWM DC/DC step-down regulator. It has a wide input range of 4V-40V and is widely used in applications from industrial to automotive for power regulation of unregulated power supplies. The regulator has a standby current of 30µA in sleep mode, which is suitable for battery-operated systems. The ultra-low full-temperature 3µA current further extends the battery life in shutdown mode. The operating frequency is fixed at 2.1MHz and the BST14006X has a fixed operating frequency of 1.1 MHz, allowing the use of small external components while still having a low output ripple voltage. The soft-start and compensation circuits are implemented internally, which allows the device to be used with minimized external components. The BST14006 is optimized for load currents up to 600mA with a typical feedback voltage of 0.765V. The device has built-in protection features such as over-temperature protection, over-current protection, and short-circuit protection. The BST14006 is available in a CLCC06 package. The BST14006X and BST14006 Y are available in a DFN06 package.

2. Features

- Input voltage range: 4V~40V.
- Up to 600mA output current.
- 30µA ultra-low quiescent current.
- · High voltage enable input.
- BST14006 \14006YDFN6E has a switching frequency of 2.1MHz, and BST14006X DFN6E has a switching frequency of 1.1MHz.
- Internal soft start.
- · Internal compensation.
- Support high duty cycle operation.
- 3µA shutdown current at full temperature.
- Over-temperature protection, over-current protection and short-circuit protection.
- BST14006 is available in CLCC06 package, while BST14006X and BST14006 Y are available in DFN06 package.



3. Functional Block Diagram

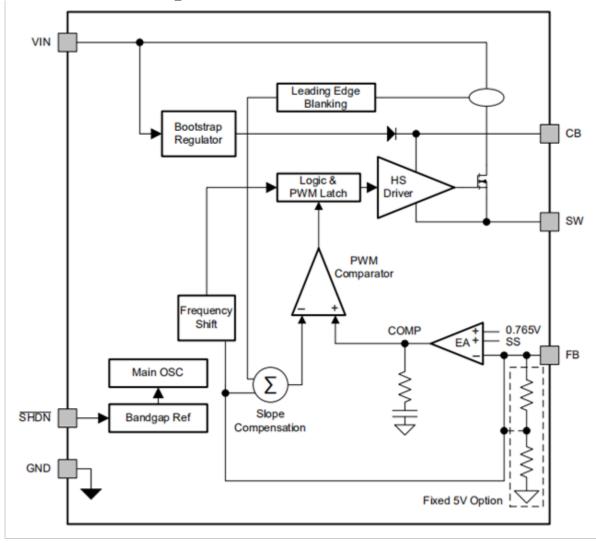


Figure 1. Functional Block Diagram

4. Pin Information Pin Assignment Diagram

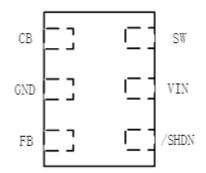


Figure 1. BST14006 pinout diagram



Pin Description

Table 1. 1Pin Description

Pins					
name	serial number	Function	describe		
СВ	1	I	SW FET gate bias voltage. Connect Cboot between CB and SW		
GND	2	G	Ground connection		
FB	3	I	The feedback divider ratio is set by V OUT = V FB (1+(R1/R2))		
/SHDN	4	I	Enable pin (high voltage tolerant). Internal pull-up current source. Pull below 1.2V to shut down. Pin can be left floating, enabled by default. Input undervoltage lockout adjustable with two resistors.		
VIN	5	I	Power supply input voltage pin. Internal power supply and drain node input for the internal high-side MOSFET		
SW	6	О	Switching node. Connects the inductor, diode and Cboot capacitor		

5. Absolute Maximum Ratings and Recommended Operating Ranges

Absolute Maximum Ratings

Input voltage (VIN to GND)	0.3V ~+ 45V
Input voltage (SHDN to GND)	0.3V~+45V
Input voltage (FB to GND)	0.3V~+7V
Input voltage (CB to SW)	0.3V ~+ 7V
Output voltage (SW to GND)	1V ~+ 45V
Output voltage (SW to GND, less than 30ns transient)	2V ~ +45V
Working junction temperature (T J)	40°C~+150°C

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating conditions for extended periods may affect device reliability and lifetime.

Note 2: This IC includes overtemperature protection to protect the device during momentary overload conditions. When the overtemperature protection is activated, the junction temperature will exceed 150°C. Continuous operation above the specified maximum operating junction temperature may impair device reliability.



6. Electrical characteristics parameters

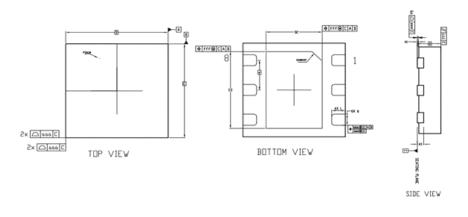
Table 2. Electrical properties

characteristic sym		condition (Unless otherwise specified, V IN = 12V, TA = 25°C)	Minimum	typical	maximu m	unit		
Input								
Input voltage	VIN		4	-	40	V		
Shutdown current	I SHDN	VEN = 0V	-	1	3	μA		
Quiescent Current	Q	No load, V _{IN} = 12V	-	28	-	μA		
	UVLO	Rising Threshold	-	-	4	V		
Undervoltage protection		Falling Threshold	3	-	-			
		Enable end						
Rising Threshold Voltage	V SHDN- Th		1.05	1.25	1.38	V		
Enable input current	/ SHDN_PIN	2.3V	-	-4.2	-	μΑ		
	/ SHDN_PIN	0.9V	-	-1	-	μΛ		
Enable input hysteresis current			-	-3	-	μA		
		High-side MOSFET						
On-resistance	R DS_ON	VIN = 12V, CB to SW = 5.8V	-	600	-	mΩ		
		Feedback						
Feedback voltage V _{FB}			0.747	0.765	0.782	V		
		Current Limit						
Current Limit / LIMIT		V IN = 12 V, T J = 25°C	-	1200	-	m A		
		S W end						
Switching frequency	f	BST14006 BST14006YDFN6E	1 785	2 100	2415	- k Hz		
Cintorning froquently	,	BST14006XDFN6E	9 35	1 100	1 285	17.172		
Minimum on-time T_{ON_MIN} $f_{SW} = 2.1 MHz (BST14006)$ $f_{SW} = 1.1 MHz (BST14006X)$		$f_{SW} = 2.1 \text{MHz (BST14006)}$ $f_{SW} = 1.1 \text{MHz (BST14006X)}$	-	1 30	-	n s		



7. Dimensions

Encapsulation



DIM	MIN.	NOM.	MAX.		
	0.70	0.75	0.80		
A	0.80	0.85	0.90		
A1	0	0.02	0.05		
A3	-	0.20 REF	-		
b	0.30	0.35	0.40		
D	3.00BSC				
Ε	3.00BSC				
e	0.95BSC				
L	0.35	0.40	0.45		
DS	1.55	1.65	1.75		
ES	2.35	2.45	2.55		
000	aaa 0.15				
blob	0.10				
ccc	0.10				
ddd	0.05				
eee	0.08				
fff		0.10			

Figure 3. 2Plastic package dimensions

8. Ordering Information

Table 3. Product Ordering Information

Serial number	model	Package	Packaging materials	Device level	Detailed specifications	Product Status
3	BST 14006YDFN6E	DFN6	Plastic packaging	Military temperature level	Q/BST 50287-2023	Mass production

Note 1:

Military temperature grade devices meet the screening requirements of Q/BST 50287-2023 "General Specifications for Huawei Military Temperature Grade Products", and their operating environment temperature range should be -55 °C \sim + 125 °C. Note 2: The product ordering information is for our existing products or devices that have been confirmed to be developed. We can develop devices in other packaging forms according to user needs.