

# BST/ (20-50) -461-75 EMI Filter Use Manual

### **Product overview**

The function of BST/ (20-50) -461-75 EMI filter is to prevent the conduction of common mode and differential mode noise between the primary power supply and the secondary power supply, and to meet the requirements of CE102 for system electromagnetic compatibility. According to the input current of the DC/DC converter used in the system at the low end of the input, and considering the design margin, select the appropriate filter type. The device has an input voltage of 0 V  $\pm$  50 V, an output current of 1.5A, and no internal surge current suppression.

### **Product features**

- Input voltage range: 20 V to 50 V, nominally 28 V and 42 V;
- Output current: 1.5A;
- Operating temperature (TC): -55 °C ~ + 125 °C;
- Circuit structure: common mode filter circuit, differential mode filter circuit;
- Quality grade: CAST C/SAST;
- Irradiation index: immunity;
- Overall dimensions: UPP3728 type metal dual in-line housing with mounting fixed end, maximum dimensions 51.00 mm x 28.94 mm x 8.38 mm.
- Weight: 36g ± 5g.

### Conditions of use

Absolute maximum:

Input voltage: 0 V ± 60 V;



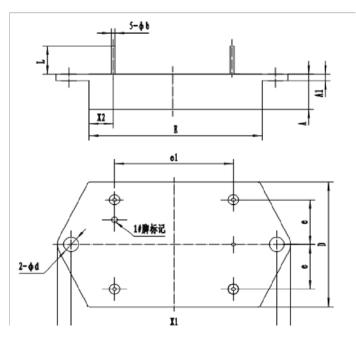
- Shell working temperature: -55 °C ~ 125 °C;
- Storage temperature: -65 °C ~ 150 °C;
- Welding temperature of lead: 300 °C (10 s);

Note: Two or more absolute maximum rating conditions cannot be applied to the device at the same time.

Recommended operating conditions:

- Input voltage: 20 V ± 50 V;
- Output current: 1.5A;
- Enclosure operating temperature range (TC): -55 °C to 125 °C.

# **Structure Description**





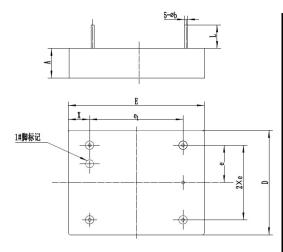
Dimension symbol	Value in mm					
Dimension symbol	Minimal	Nominal	Max.			
A	-	-	8.38			
A1	1.30	-	1.70			
фЬ	0.63	-	0.89			
фd	3.05	-	3.45			
D	-	-	28.94			
E	-	-	37.33			
e	-	10.16	-			
e1	-	25.40	-			
L	5.35	-	-			
X	-	-	51.00			
X1	43.75	-	44.15			
X2	4.91	-	5.51			

Note: e and E1 are interchangeability dimensions, which are guaranteed by shell manufacturing and inspection, and are not required to be examined in this specification.

Figure 1. BST/ (20-50) -461-75 Outline Drawing



# **Structure Description**



Dimension	Value in mm					
symbol	Minimal	Nominal	Max.			
А	-	-	8.38			
фЬ	0.63	-	0.89			
D	-	-	28.94			
E	-	-	37.33			
е	-	10.16	-			
e1	-	25.40	-			
L	5.35	-	-			
X	4.91	-	5.51			

Note: e and E1 are interchangeability dimensions, which are guaranteed by shell manufacturing and inspection, and are not required to be examined in this specification.

Figure 2. BST/ (20-50) -461-75-A Outline Drawing



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	底视图	3 +
<b>⊕</b> 5		4 ↔

NO.	Symbol	Function	Number of outlet	Symbol	Function
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1	VI	Input positive	4	GNDO	Outgoing
2	VO	Output positive	5	GNDI	Input Ground
3	CASE	Enclosure			

Figure 3. BST/ (20-50) -461-75, BST/ (20-50) -461-75-A Terminal Arrangement

# **Functional Block Diagram**

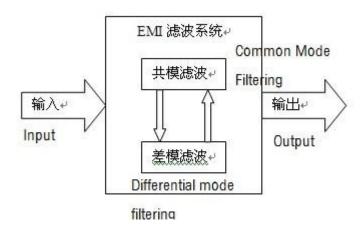


Figure 3. Functional Block Diagram



# **Electrical Characteristic Index**

Table 1. Electrical characteristics (BST/ (20-50) -461-75)

			Condition (Unless			Limit Val	ue		
NO.	Characteristic	Symbol	otherwise specified, $-55 ^{\circ}\text{C} \le \text{TC} \le 125 ^{\circ}\text{C}$ VI = 28V $\pm$ 0.5V and VI = 42V $\pm$ 0.5V)	Group A Grouning	griduoio	Minimal	Max.	Measure d Value	Unit
1	Input Voltage Range a	VI	Continuity	1,2,3		0	50	0-50	V
2	Output current a	Ю	VI = 20Vx50V, full load	1,2,3		-	1.5	1.5	Α
3	Output Voltage Dip	VOD	VI = 20V, 28V, 42V, 50V full load	1		-	0.525	0.43	V
			Test frequency 1 kHz	4,5,6		-1	1	0.6	
4	Noise Suppression	NO	Test frequency 500 kHz	4,5,6		50	-	60	dB
			Test frequency: 1MHz	4,5,6		55	-	63	



			Test frequency: 5MHz	4,5,6	50	-	63	
5	DC resistance	RDC	IO=1.5A	1	-	0.35	0.29	Ω
6	Power consumption	PI	IO=1.5A	1	-	0.7875	0.66	W
7	Insulation Resistance	RIS O	500 V DC voltage is applied between the input and output terminals (except terminal 3) and the enclosure	1	100	-	100	ΜΩ
8	Capacitance	со	Between any outgoing terminal (except terminal 3) and the enclosure	1	-	24000	21400	pF

Note: a is verified when the test output voltage drops, and is not tested separately.



## 8. Typical application circuit

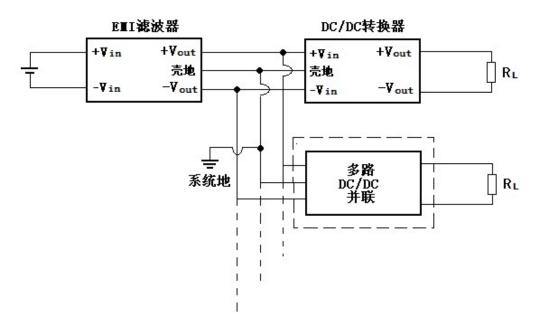


Figure 4. Typical Application Circuit

## **Description of typical application**

#### 9.1 Treatment of Shell Ground

The housing of the device must be in good contact with the system ground or the device will not have the conduction suppression it was designed for.

### 9.2 Overcurrent and short circuit precautions

The device has no short-circuit protection function. When the output positive terminal and the output negative terminal are short-circuited or the device is overcurrent, the device has no protection mode, which is easy to cause thermal burnout of the device. Check whether the output terminal is short-circuited before powering on. If yes, the power can be turned on only after the fault is removed.

### 9.3 Overvoltage/undervoltage protection

The device has no input overvoltage and output overvoltage protection, so attention shall be paid during use.



### 9.4 Reliability recommendations

- Pay attention to the working voltage, working current and leakage current when selecting the filter;
- Thermal performance data: the maximum temperature rise of the internal magnetic core is 7.44 °C, the power consumption of the device is 4.5 W, and the internal thermal resistance is 1.65 °C/W at Tc = 60 °C (the temperature of the temperature control table is 60 °C), 28V input and full load output.
- The device is packaged with salt-fog resistant metal (10 # steel), and the device can be coated with "three-proof" paint to enhance the adaptability of the system to harsh environments;

### 9.5 Installation Precautions

- Before installation, check whether the appearance of the module is deformed, the surface is rusted, and the insulator is broken.
- The outer pins of the module are mainly used for electrical connection and are not allowed to be used as mechanical supports for the module.
- When the module should be fixed by mechanical reinforcement, the module must be fixed first, and then the soldering between the pins and the PCB must be carried out, and the operation sequence cannot be reversed.
- There shall be no raised through hole or solder joint on the PCB board contacting with the bottom of the module, and the heat dissipation device shall be flat to avoid affecting the contact.
- Pay attention to electrostatic protection during installation.
- The installation principle of the filter is to effectively isolate the main power supply from the DC/DC power supply. If the EMI filter is installed incorrectly, the performance of the filter will not work properly, or even have a negative effect. Only when the filter is installed properly can good results be achieved:



- It is better to install the filter at the outlet of the interference source (power supply), and then shield the interference source and the filter. If the inner cavity space of the interference source is limited, the filter shell and the interference source shell shall be well overlapped near the outer side of the power line outlet of the interference source;
- The input and output lines of the filter must be separated to prevent the
  coupling between the input line and the output line and reduce the
  characteristics of the filter. A clapboard-type chassis is usually used to
  fix the filter, and if an isolation method cannot be implemented,
  shielded leads are used to reduce common impedance coupling;
- The connection of the filter is preferably twisted-pair, which can eliminate part of the high-frequency interference;
- The low frequency characteristic of the filter is related to the volume, and if the low frequency characteristic is desired to be good, the volume is generally sacrificed;
- The grounding wire of the filter shall be as short as possible to
  minimize the electromagnetic coupling between the input end and the
  output end of the filter without damaging the suppression effect of the
  shielding structure of the system equipment on the electromagnetic
  interference noise.

An ideal EMI power filter installation is shown in Figure 14.

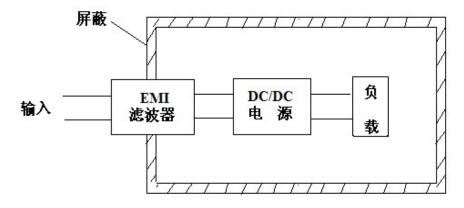


Figure 5. Installation method of ideal EMI power filter



## Characteristic curve

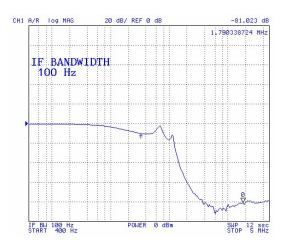


Figure 6. BST/ (20-50) -461-75 Insertion Loss

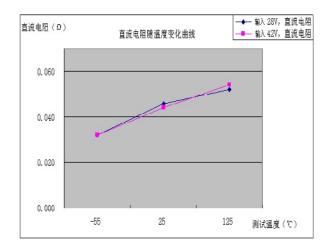


Fig.7. DC Resistance-Temperature Curve

## Limiting characteristic

- Electrical stress limit characteristics: maximum input voltage 80 V,
   minimum input voltage 0 V, insulation resistance up to 800 V.
- Temperature limit characteristics: the maximum working temperature can reach 150 °C, the minimum working temperature can reach -65 °C, and it can pass 100 times of temperature cycle and 100 times of temperature shock test in GJB548B-2005 method.





 Mechanical limit characteristics: it can pass the mechanical shock test under condition D and random vibration test under condition F in GJB548B-2005.